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<u>#2</u>	(((power <sentence> (consume <or> consuming <or> consumption <or> use <or> usage <or> using)) <sentence> (clock <near/4> (cycle <or> cycles <or> frequency <or> frequencies))))<in>metadata)	147
<u>#3</u>	(((power <sentence> (consume <or> consuming <or> consumption <or> use <or> usage <or> using)) <sentence> (clock <near/4> (cycle <or> cycles <or> frequency <or> frequencies)) <sentence> (increase <or> increasing <or> increases))))<in>metadata)	848670
<u>#4</u>	#1	848670

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#2	(((power <sentence> (consume <or> consuming <or> consumption <or> use <or> usage <or> using)) <sentence> (clock <near/4> (cycle <or> cycles <or> frequency <or> frequencies)))<in>metadata)	147
#3	(((power <sentence> (consume <or> consuming <or> consumption <or> use <or> usage <or> using)) <sentence> (clock <near/4> (cycle <or> cycles <or> frequency <or> frequencies)) <sentence> (increase <or> increasing <or> increases)))<in>metadata)	8
#4	#1	848670
#5	(((speculative <or> future) <near/4> (register <or> registers <or> cache <or> caches <or> buffer <or> buffers)) <sentence> (restore <or> restores <or> restoring <or> refill <or> refills <or> refilling) <sentence> clock)<in>metadata)	1
#6	(((speculative <or> future) <near/4> (register <or> registers <or> cache <or> caches <or> buffer <or> buffers)) <sentence> (restore <or> restores <or> restoring <or> refill <or> refills <or> refilling))<in>metadata)	1
#7	(((speculative <or> future) <near/4> (register <or> registers <or> cache <or> caches <or> buffer <or> buffers)) <sentence> (restore <or> restores <or> restoring <or> refill <or> refills <or> refilling))<in>metadata)	1
#8	(((speculative <or> future) <sentence> (register <or> registers <or> cache <or> caches <or> buffer <or> buffers)) <sentence> (restore <or> restores <or> restoring <or> refill <or> refills <or> refilling))<in>metadata)	3

Clock Speed - PC ComputerNotes

The **clock** speed of a CPU is defined as the **frequency** that a processor ... This **clock** speed is measured in millions of **cycles** per second or megahertz (MHz). ...

www.pccomputernotes.com/clockspeed/clockspeed.htm - 12k - [Cached](#) - [Similar pages](#)

Functional verification of a frequency-programmable switch chip ...

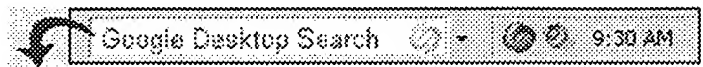
In a simulation **cycle** environment, the multiple **frequency clock** domains are represented as simulation **cycles** per system **clock cycle** relationships. ...

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Clock frequency + clock cycle

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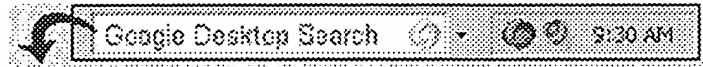
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